PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/930607		~~325~~ 7̶1̶7 716	9 17	~~2172~~ 2825	Powell Mark. R BT-N4 T AT

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TITLE
Instruction processor and programmable logic device cooperative computing arrangement and method

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)				
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION							

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☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Primary Examiner) _____ (Date)			ISSUE FEE	
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